

Chandra Kumar R

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I have **12+ years** of experience in development and maintenance of system software tools (compiler, assembly to assembly translator, debugger and IDE).

Summary:

- Developed Samsung Convolutional Deep Neural Network Model Visualizer using Eclipse RCP .
- Developed Profiler for Samsung Reconfigurable Processor (SRP)
- Developed Integrated Development Environment (IDE) for SRP using Eclipse CDT.
- Ported LLDB Debugger for SRP.
- Maintained GNU tool chain for RISC processor (ABACUS).
- Developed assembly translator for CISC micro-controllers.
- Developed DSP-C compiler back-end optimizations (Instruction level parallelism, Pipeline hazards optimization).
- Validated compiler for RISC micro controller.

Technical Skills:

- Hands-on experience in Eclipse IDE, LLDB debugger framework, GDB debugger framework and GCC compiler generation framework.
- Hands-on experience in compiler back-end optimizations (Instruction level parallelism & pipeline hazards optimization).
- Proficient in data structures and Object Oriented Programming concepts.
- Expertise in C, C++ and Assembly programming.

Accomplishments:

- A patent on "Source level debugger for Reconfigurable Processor".
- Samsung Employee of the Month award in 2015 for SRP IDE work.
- Samsung Breakthrough Team Award in 2013 for SRP SDK work.
- Samsung SPOT award in 2012 for the SRP debugger work.
- Our work on assembly translator prototype got customer appreciation and complete project development to acmet.
- "Excellent role model" appreciation by Vice-president of Tools Business Unit, acmet.

Employment Summary

| ORGANIZATION | POSITION | TENURE | AREA OF WORK |
|-----------------------------------|------------------------------|------------------------------|---|
| Samsung Research India, Bangalore | Chief Engineer | May 2012 to Present | Visualizer of Convolutional Neural Network Model. LLDB based debugger, Profiler and IDE for SRP. |
| | Technical Lead | July 2011 to April 2012 | GDB porting for SRP. |
| AMI India Pvt. Ltd. | Technologist | April 2010 to June 2011 | Maintenance of GNU tool chain for a RISC processor. |
| Acme Technologies Pvt. Ltd. | Team Leader | October 2007 to March 2010 | Development of assembly translator. (translation of one CISC assembly to other CISC assembly) GCC Prototype compiler for a VLIW DSP processor. |
| | Senior Software Engineer | May 2005 to September 2007 | Back-end development and maintenance of DSP C compiler. (Instruction level parallelism, Pipeline hazards optimization) |
| | Software Engineer | September 2004 to April 2005 | QA of a compiler developed for an 8-bit RISC controller. |
| | Software Engineering Trainee | July 2004 to August 2004 | Learning process and practices, testing methodologies for compiler. |

[More...](#)

Education

| COURSE | INSTITUTION | YEAR OF PASSING | PERCENTILE MARKS/ CGPA |
|------------------------|--|------------------------|-------------------------------|
| M.Tech (WILP) | BITS Pilani | 2017 (In progress) | 9.3 |
| M.B.A (Distance Mode) | Anna University, India | 2009 | 63 |
| B.E (Computer Science) | Manonmaniam Sundaranar University, India | 2004 | 73.5 |
| HSC | Concordia Higher Secondary School, Vallioor, Tamil Nadu | 2000 | 80 |
| SSLC | Government Higher Secondary School, Vallioor, Tamil Nadu | 1998 | 87.4 |